

CBCS SCHEME

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BCS303

Third Semester B.E/B.Tech. Degree Examination, Dec.2025/Jan.2026 Operating Systems

Time: 3 hrs.

Max. Marks:100

Note: 1. Answer any FIVE full questions, choosing ONE full question from each module.
2. M : Marks , L: Bloom's level , C: Course outcomes.

Module – 1			M	L	C												
1	a.	Explain the various types of system calls with an example for each.	8	L2	CO1												
	b.	Explain cloud computing its types and their services it offers.	8	L2	CO1												
	c.	What is dual mode operation and what is the need of it?	4	L2	CO1												
OR																	
2	a.	Explain the different architecture of OS starting from Simple Structure, Layered Structure, Micro Kernels, Modules and Hybrid Systems.	8	L4	CO1												
	b.	Discuss the essential properties of the following types of systems : i. Time sharing system ii. Multi-programmed batch systems.	6 6	L2	CO1												
Module – 2																	
3	a.	Explain Inter Process Communication.	6	L2	CO2												
	b.	Discuss Multilevel Queue Scheduling Algorithm.	6	L4	CO2												
	c.	Consider the set of 3 processes whose arrival time and burst time are given below. <table border="1"><thead><tr><th>Process Id</th><th>Arrival Time</th><th>Burst Time</th></tr></thead><tbody><tr><td>P1</td><td>0</td><td>9</td></tr><tr><td>P2</td><td>1</td><td>4</td></tr><tr><td>P3</td><td>2</td><td>9</td></tr></tbody></table> If the CPU scheduling policy is SRTF, calculate the average waiting time and average turnaround time.	Process Id	Arrival Time	Burst Time	P1	0	9	P2	1	4	P3	2	9	8	L3	CO2
Process Id	Arrival Time	Burst Time															
P1	0	9															
P2	1	4															
P3	2	9															

1 of 3

OR

4	a.	Compare User Level Threads and Kernel Level threads.	4	L4	CO2																					
	b.	Illustrate with a neat sketch, Process States and Process Control Block (PCB).	8	L2	CO2																					
	c.	Consider the set of 6 processes whose arrival time and burst time are given below. <table border="1"><thead><tr><th>Process ID</th><th>Arrival Time</th><th>Burst Time</th></tr></thead><tbody><tr><td>P₁</td><td>5</td><td>5</td></tr><tr><td>P₂</td><td>4</td><td>6</td></tr><tr><td>P₃</td><td>3</td><td>7</td></tr><tr><td>P₄</td><td>1</td><td>9</td></tr><tr><td>P₅</td><td>2</td><td>2</td></tr><tr><td>P₆</td><td>6</td><td>3</td></tr></tbody></table> If the CPU scheduling policy is Round Robin with time quantum = 3, calculate Average Waiting time and Turnaround time.	Process ID	Arrival Time	Burst Time	P ₁	5	5	P ₂	4	6	P ₃	3	7	P ₄	1	9	P ₅	2	2	P ₆	6	3	8	L3	CO2
Process ID	Arrival Time	Burst Time																								
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P ₆	6	3																								

Module – 3

5	a.	Discuss in detail the critical section problem and write the algorithm for producer consumer problem.	10	L2	CO3																																																																					
	b.	Consider the following system using data structures in the Bankers Algorithm with resource type ABC Maximum instance present in the system A = 10, B = 5, C = 7. <table border="1"><thead><tr><th rowspan="2">Process</th><th colspan="3">Allocation</th><th colspan="3">Max</th><th colspan="3">Available</th></tr><tr><th>A</th><th>B</th><th>C</th><th>A</th><th>B</th><th>C</th><th>A</th><th>B</th><th>C</th></tr></thead><tbody><tr><td>P₀</td><td>0</td><td>1</td><td>0</td><td>7</td><td>5</td><td>3</td><td>3</td><td>3</td><td>2</td></tr><tr><td>P₁</td><td>2</td><td>0</td><td>0</td><td>3</td><td>2</td><td>2</td><td></td><td></td><td></td></tr><tr><td>P₂</td><td>3</td><td>0</td><td>2</td><td>9</td><td>0</td><td>2</td><td></td><td></td><td></td></tr><tr><td>P₃</td><td>2</td><td>1</td><td>1</td><td>2</td><td>2</td><td>2</td><td></td><td></td><td></td></tr><tr><td>P₄</td><td>0</td><td>0</td><td>2</td><td>4</td><td>3</td><td>3</td><td></td><td></td><td></td></tr></tbody></table> i. Calculate Need Matrix ii. Check whether system is safe or not.	Process	Allocation			Max			Available			A	B	C	A	B	C	A	B	C	P ₀	0	1	0	7	5	3	3	3	2	P ₁	2	0	0	3	2	2				P ₂	3	0	2	9	0	2				P ₃	2	1	1	2	2	2				P ₄	0	0	2	4	3	3				10	L3	CO3
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OR

6	a.	Outline the solutions of Dining –Philosopher problem.	5	L2	CO3																																																																																										
	b.	Describe a resource allocation graph with an example.	5	L4	CO3																																																																																										
	c.	Using Bankers algorithm, solve the following problem : <table border="1"><thead><tr><th rowspan="2">Process</th><th colspan="4">Allocation</th><th colspan="4">Max</th><th colspan="4">Available</th></tr><tr><th>A</th><th>B</th><th>C</th><th>D</th><th>A</th><th>B</th><th>C</th><th>D</th><th>A</th><th>B</th><th>C</th><th>D</th></tr></thead><tbody><tr><td>P₀</td><td>0</td><td>0</td><td>1</td><td>2</td><td>0</td><td>0</td><td>1</td><td>2</td><td>1</td><td>5</td><td>2</td><td>0</td></tr><tr><td>P₁</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td><td>7</td><td>5</td><td>0</td><td></td><td></td><td></td><td></td></tr><tr><td>P₂</td><td>1</td><td>3</td><td>5</td><td>4</td><td>2</td><td>3</td><td>5</td><td>6</td><td></td><td></td><td></td><td></td></tr><tr><td>P₃</td><td>0</td><td>6</td><td>3</td><td>2</td><td>0</td><td>6</td><td>5</td><td>2</td><td></td><td></td><td></td><td></td></tr><tr><td>P₄</td><td>0</td><td>0</td><td>1</td><td>4</td><td>0</td><td>6</td><td>5</td><td>6</td><td></td><td></td><td></td><td></td></tr></tbody></table> i. Calculate the Need Matrix ii. Check whether system is safe or not.	Process	Allocation				Max				Available				A	B	C	D	A	B	C	D	A	B	C	D	P ₀	0	0	1	2	0	0	1	2	1	5	2	0	P ₁	1	0	0	0	1	7	5	0					P ₂	1	3	5	4	2	3	5	6					P ₃	0	6	3	2	0	6	5	2					P ₄	0	0	1	4	0	6	5	6					10	L2	CO3
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Module – 4					
7	a.	Discuss the given memory management technique with diagram. i. Paging ii. Translation Look-Aside Buffer.	6 6	L2	CO4
	b.	Discuss about Contiguous Memory Allocation with a neat diagram.	8	L2	CO4
OR					
8	a.	Consider the reference string : 6, 1, 1, 2, 0, 3, 4, 6, 0, 2, 1, 2, 1, 2, 0, 3, 2, 1, 2, 0 For a memory with three frames and calculate number of page faults by using i. LRU replacement ii. FIFO replacement.	10	L3	CO4
	b.	Describe the process of demand paging in OS.	10	L2	CO4
Module – 5					
9	a.	Explain in detail about directory and disc structure.	6	L2	CO5
	b.	Analyze the file system implementation.	6	L4	CO5
	c.	The requested tracks, in the order received are {176, 79, 34, 60, 92, 11, 41, 114} Apply the following disk scheduling algorithms starting track at 50. i) FCFS ii) SSTF. Calculate the total seek time.	8	L3	CO5
OR					
10	a.	Explain Free Space Management with an example.	6	L2	CO2
	b.	Explain the Access Matrix method of system protection with the domain as objects and its implementation.	6	L2	CO2
	c.	The requested tracks, the order received are {176, 79, 34, 60, 92, 11, 41, 114} Apply the following disk scheduling algorithms starting track at 50. i) Look ii) C – Look. Calculate the total seek time.	8	L3	CO3
