Important Note: 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages. 2. Any revealing of identification, appeal to evaluator and /or equations written eg, 42+8 = 50, will be treated as malpractice.

USN

First Semester M.Tech Degree Examination, Dec.2014/Jan.2015 **Multicore Architecture and Programming**

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Time: 3 hrs. Max. Marks			arks: 100
		Note: Answer any FIVE full questions.	
1	a.	What are the motivations for concurrency in software? Discuss the reasons for concurrency in software?	oncurrency
		in software is important.	(05 Marks)
	b.	Describe the Flyxn's taxonomy and classification.	(08 Marks)
	c.	What is Virtualization? Describe the different virtualizations used in modern	computers. (07 Marks)
2	2 a. Describe the different decomposition techniques with example. List the im		ications of
_		decomposition techniques.	(10 Marks)
	b.	What is error diffusion? Explain the steps involved in error diffusion algo	rithm with
		example.	(10 Marks)
3	a. What are the principles used to perform synchronization? Explain them with nec		
		pseudocodes.	(10 Marks)
	b.	Describe the different models of message passing in the context of multi-	i threading
		environment.	(06 Marks)
	c.	Define the following: i) Fence ii) Barrier.	(04 Marks)
4	a.	Describe the different parameters for the basic thread creation mechanism Create	Thread ()
		provided by the Microsoft.	(10 Marks)
	b.	Write a code for communicating between threads by using windows events.	(10 Marks)
5	a.	Write a note on:	
		i) Thread priority and ii) Processor affinity.	(10 Marks)
	b.	Write a program to obtain the basic processes data from windows.	(05 Marks)
		Write a program to create fibers that print an identifying message to the console.	(05 Marks)
6	a.	What are the challenges in threading a loop? Explain any four.	(08 Marks)
	Ь	What to the need of minimizing threading averboad? List the manural refer of a	act of onen

- b. What is the need of minimizing threading overhead? List the measured costs of a set of open MP constructs on a 4 – way intel xeon processor based system. (07 Marks)
- c. With diagram, explain the Intel Task queuing extension to open MP.

(05 Marks)

- 7 a. What are the difficulties in debugging an Open MP program? Mention the guidelines for debugging Open MP program.
 - b. In parallel programming model too many threads can degrade the performance. Discuss any (10 Marks)
 - c. What are the four conditions that may lead the dead lock to occur?

(04 Marks)

- a. Discuss the reasons for locks to be heavily contented. Explain the solutions for heavily contended locks. (06 Marks)
 - b. Describe any two issues of multi core processors suppose to take care about memory.

(10 Marks)

c. Discuss the two rules that cover typical programming on IA 32 architecture. (04 Marks)